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EFS ID:	2463667				
Application Number:	10729865				
International Application Number:					
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Title of Invention:	Nand memory array incorporating multiple series selection devices and method for operation of same				
First Named Inventor/Applicant Name:	En-Hsing Chen				
Customer Number:	52100				
Filer:	Andrew C. Graham/Katrina Prati				
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)		
1	Reply Brief Filed	023-0029_Reply_Brief_2007 1113.pdf	174910 d821280addb4337de56dec05810980b cs294873	no	4		

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the International filing date of the application.